

FIG. 1A
(prior art)

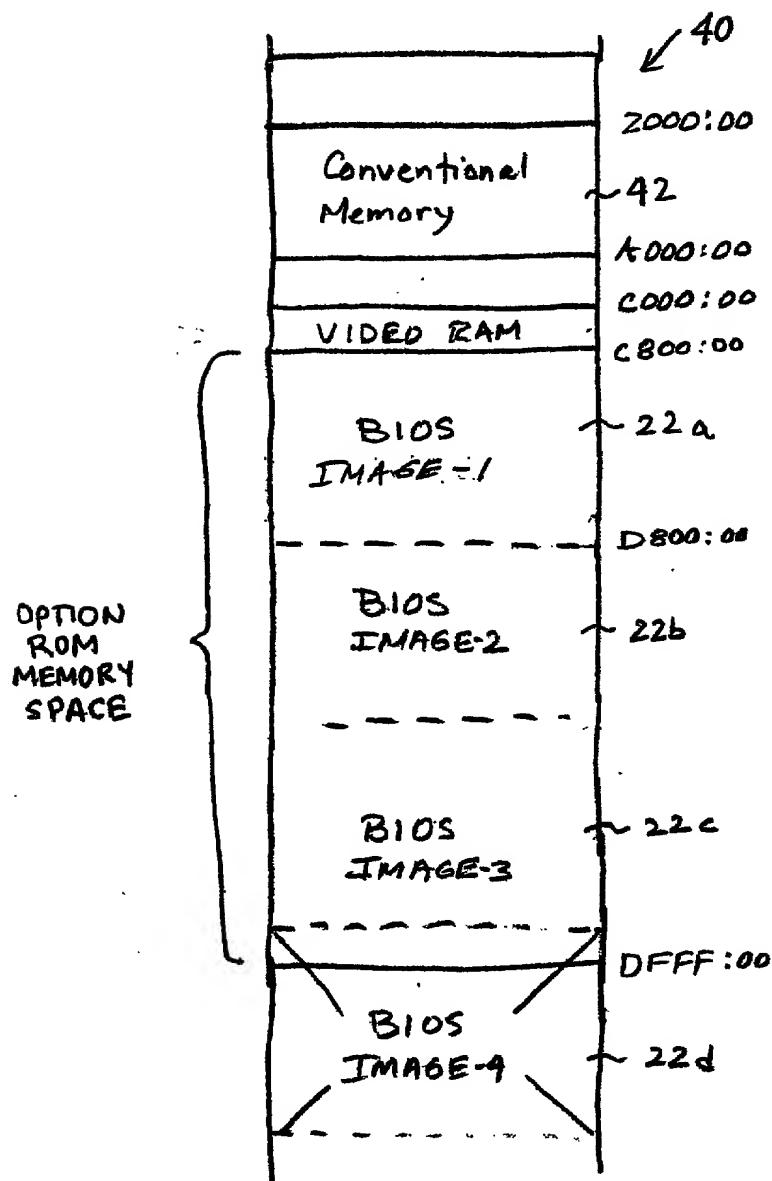


FIG. 1B
(prior art)

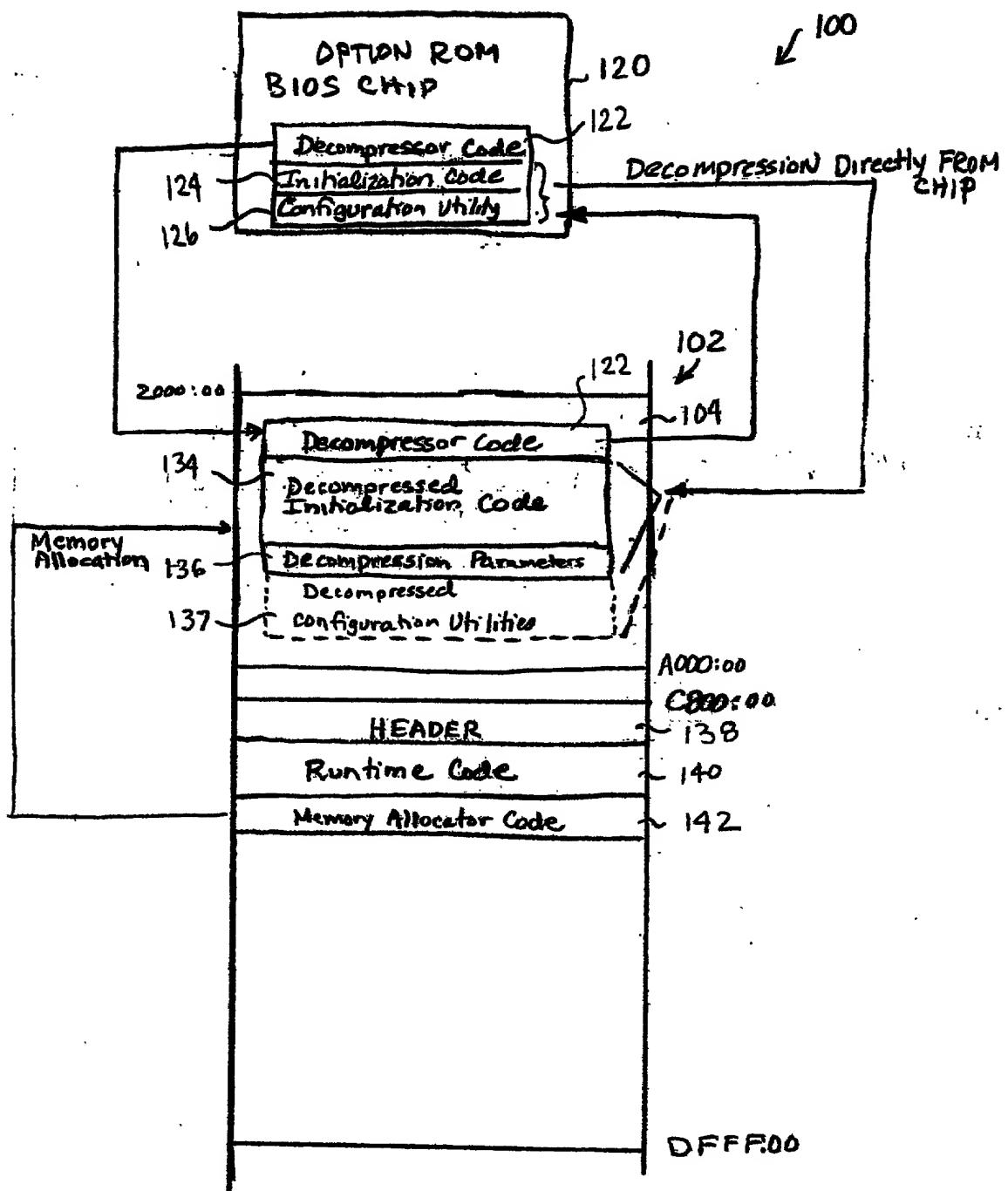


FIG. 2A

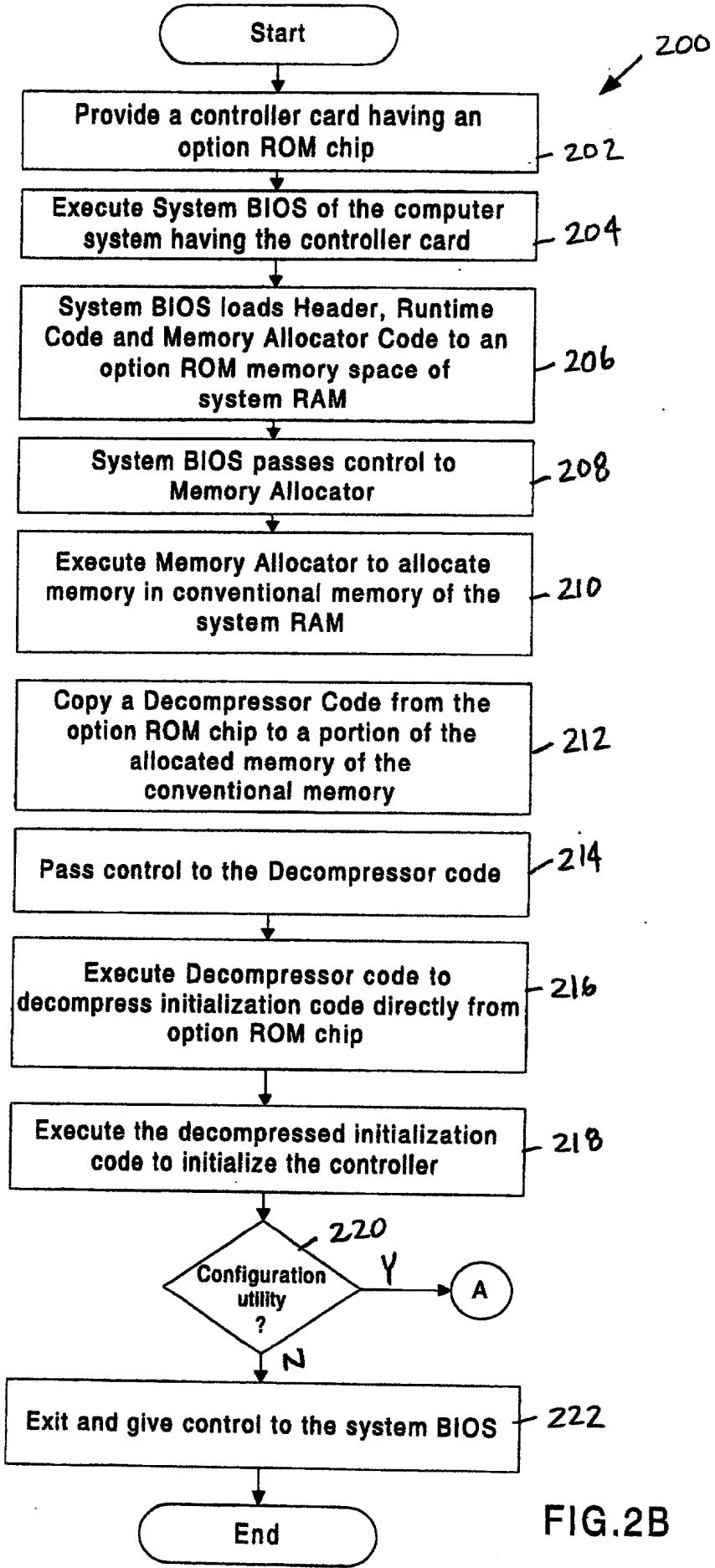


FIG.2B

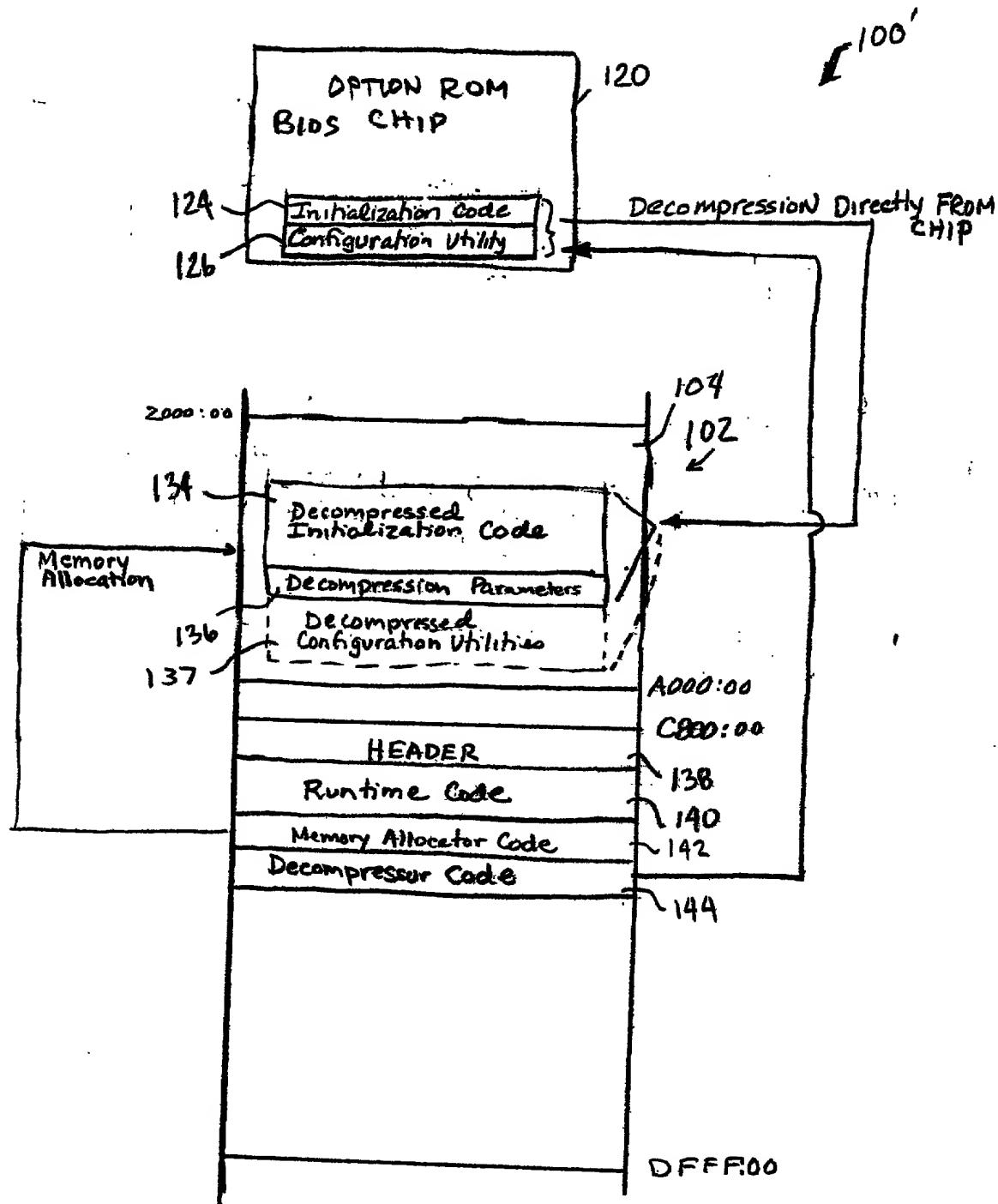


FIG. 3A

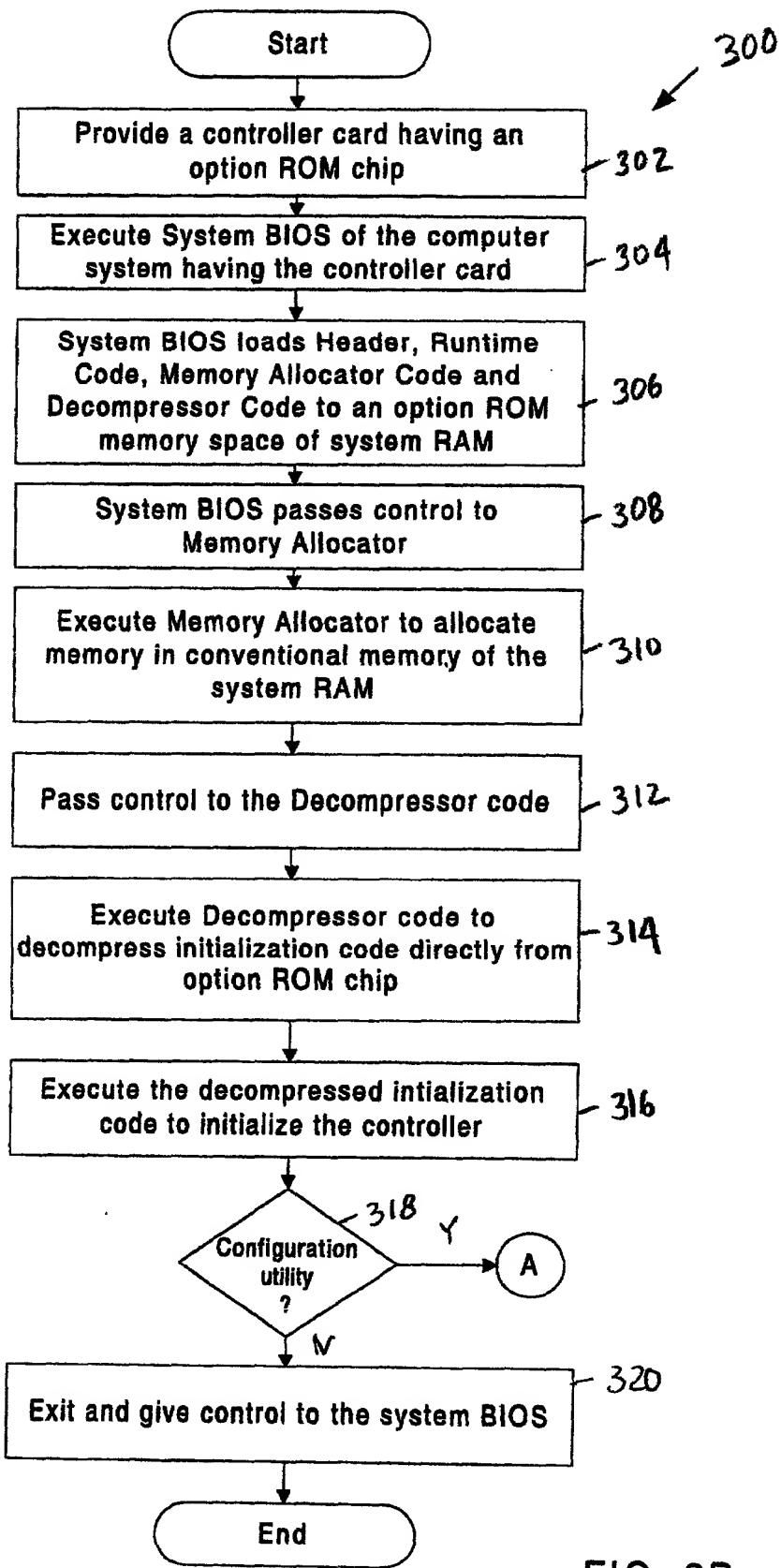


FIG. 3B

00000000000000000000000000000000

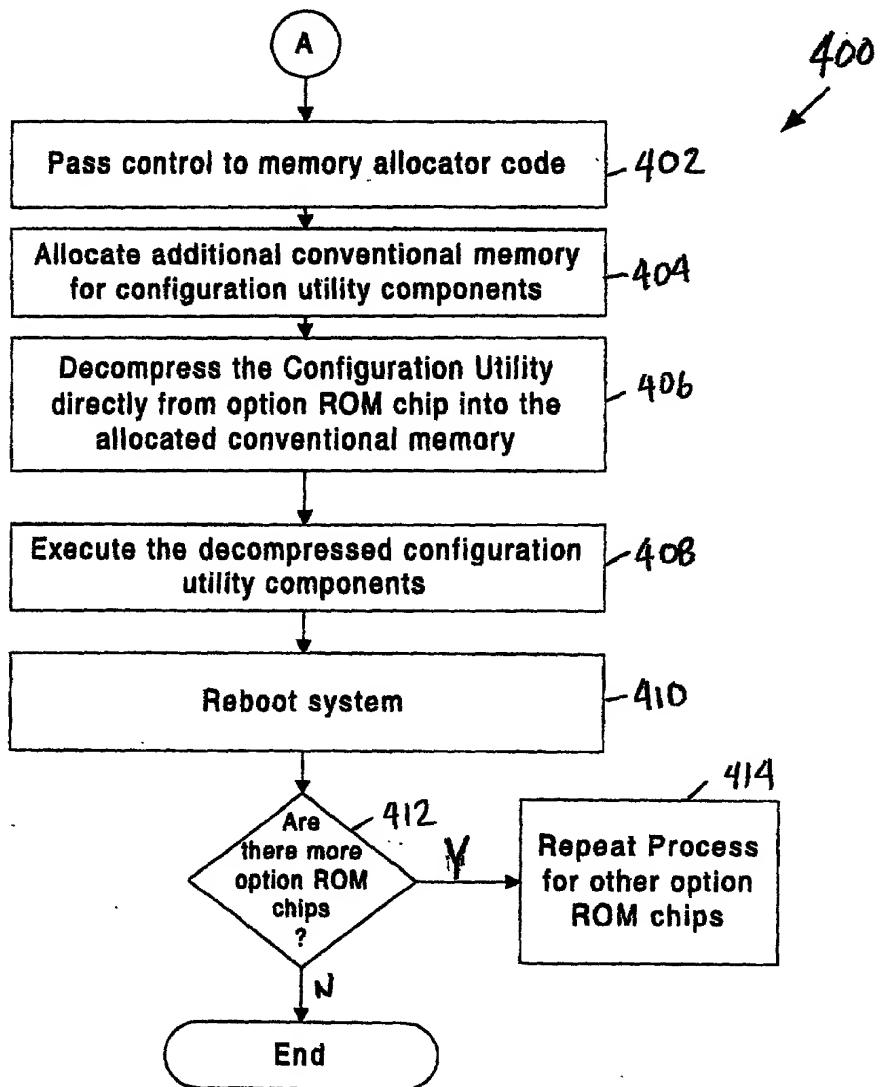
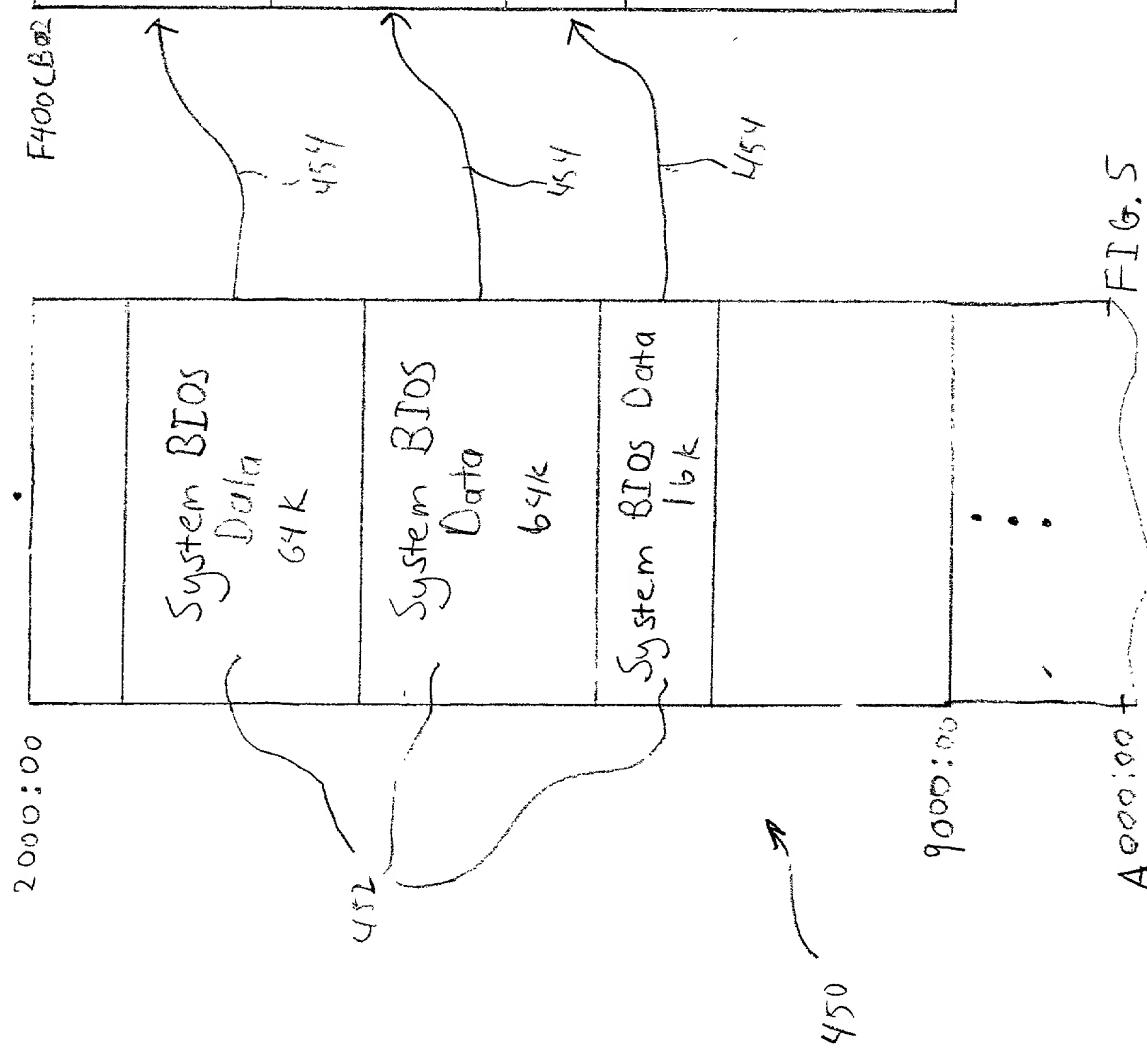


FIG.4

Conventional Memory



Extended Memory

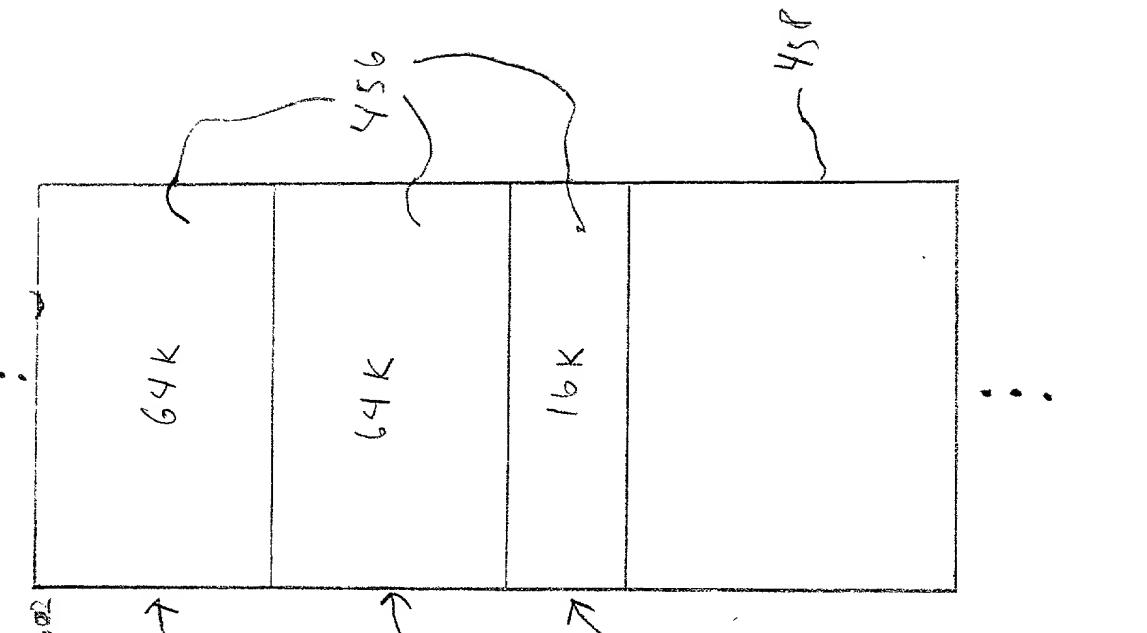


FIG. 5

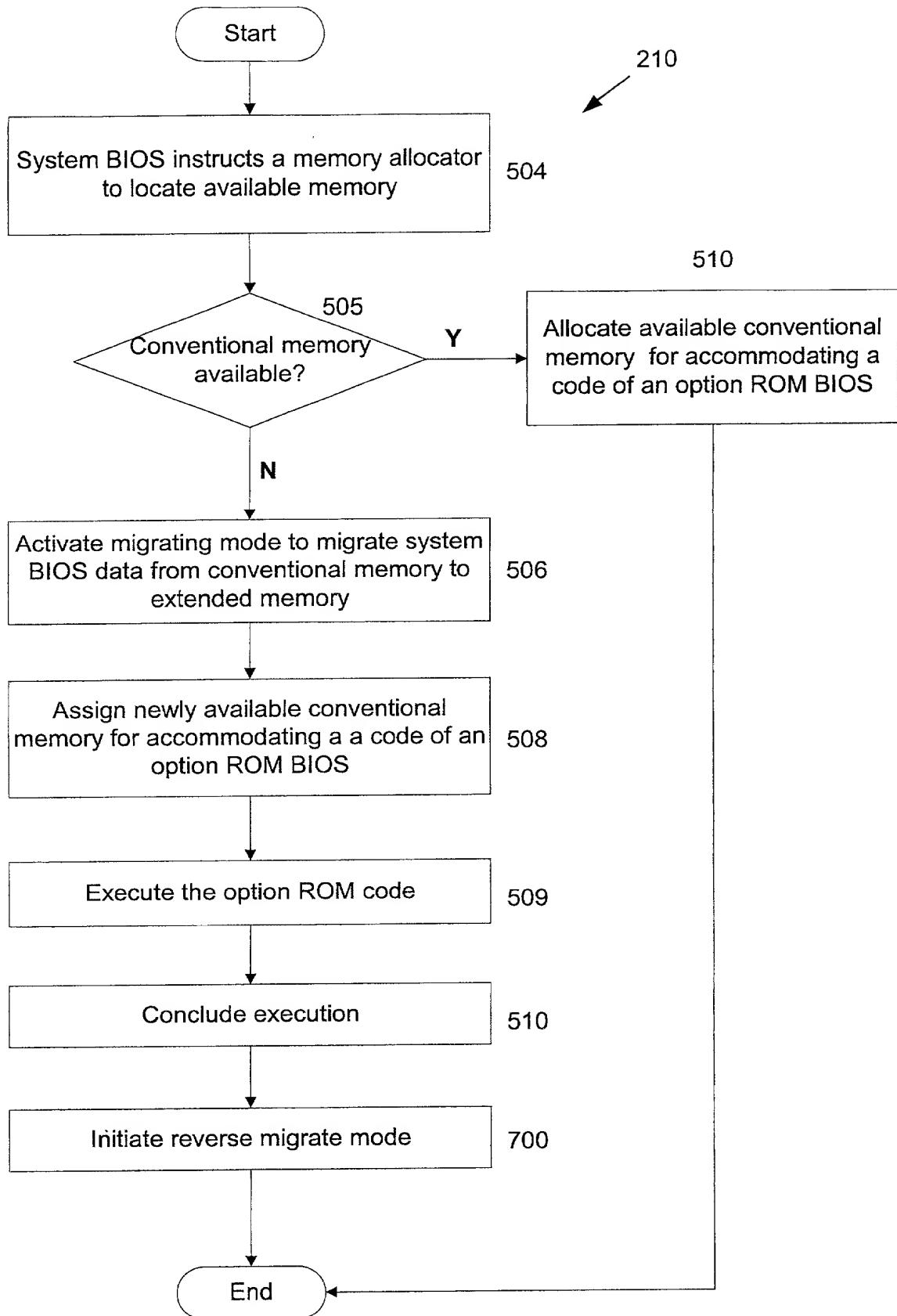


FIG. 6

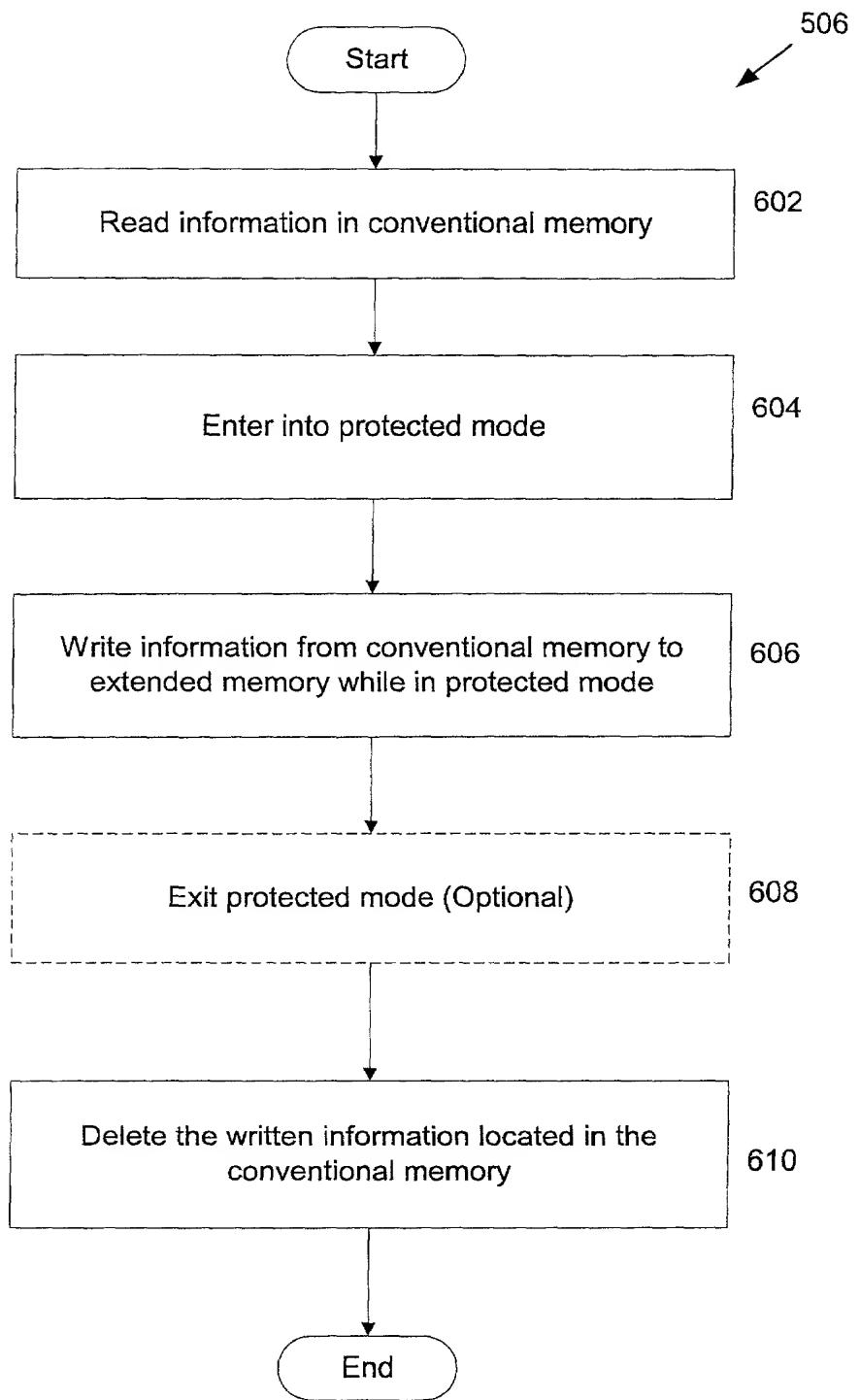


FIG. 7

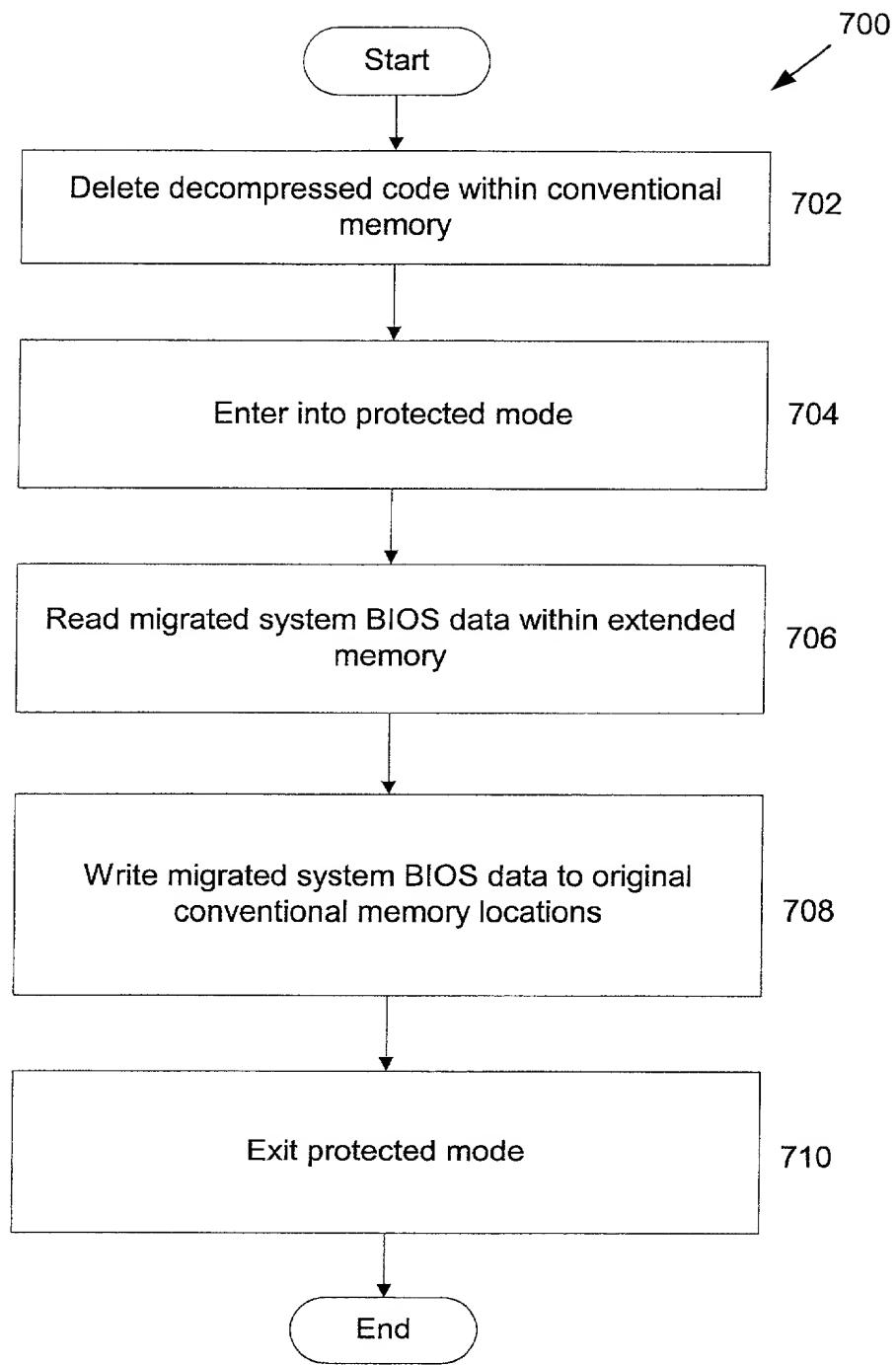


FIG. 8